Fig. 1

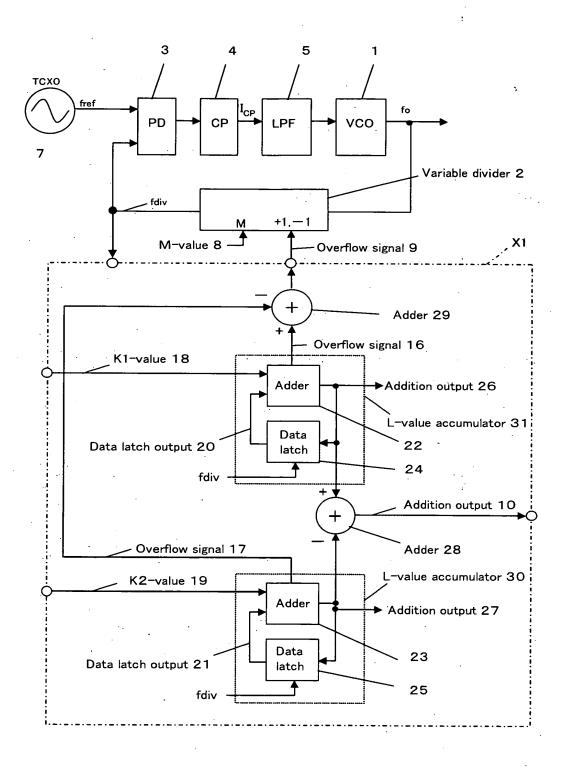


Fig. 2

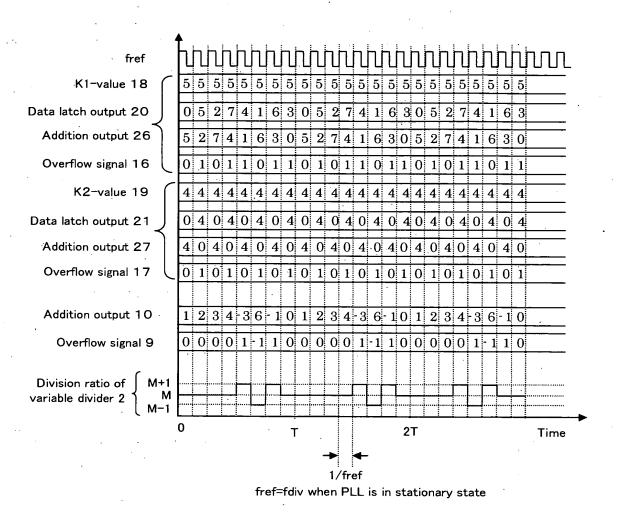


Fig. 3

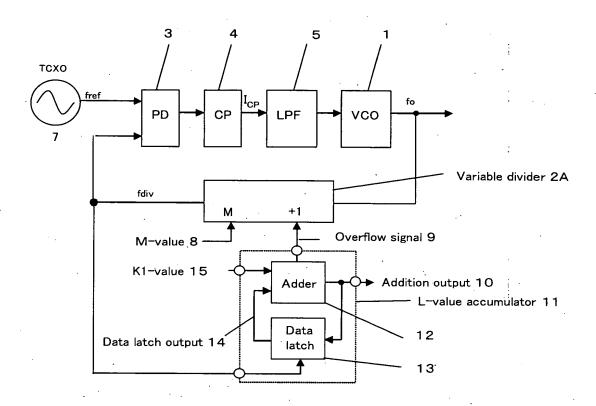
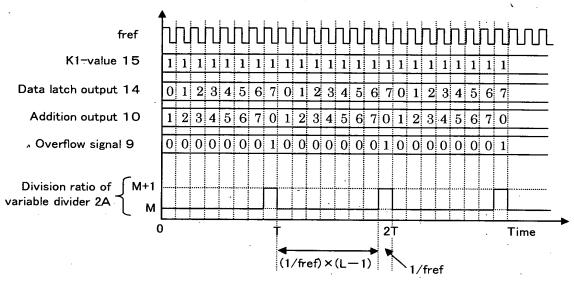


Fig. 4



fref=fdiv when PLL is in stationary state

Fig. 5

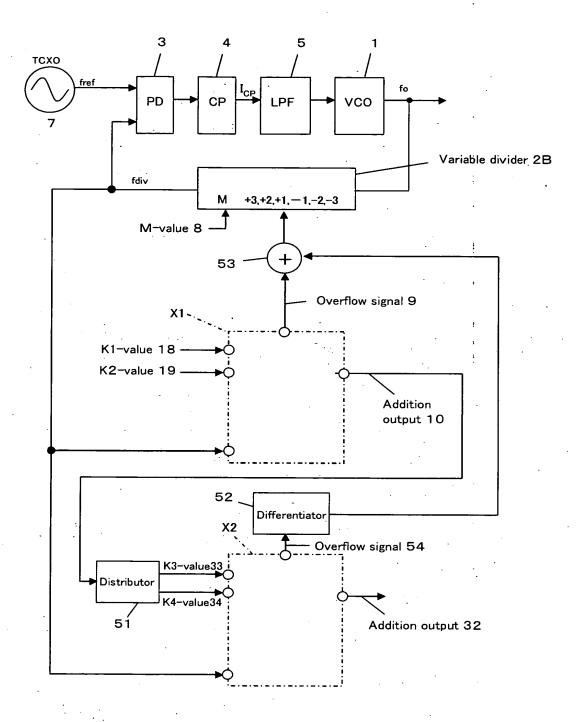


Fig. 6

